LM723/LM723C Voltage Regulator

FEATURES

- 150 mA Output Current Without External Pass Transistor
- Output Currents in Excess of 10A Possible by Adding External Transistors
- Input Voltage 40V Max
- Output Voltage Adjustable from 2V to 37V
- Can be Used as Either a Linear or a Switching Regulator

DESCRIPTION

The LM723/LM723C is a voltage regulator designed primarily for series regulator applications. By itself, it will supply output currents up to 150 mA; but external transistors can be added to provide any desired load current. The circuit features extremely low standby current drain, and provision is made for either linear or foldback current limiting.

The LM723/LM723C is also useful in a wide range of other applications such as a shunt regulator, a current regulator or a temperature controller.

The LM723C is identical to the LM723 except that the LM723C has its performance ensured over a 0 °C to +70 °C temperature range, instead of −55 °C to +125 °C.

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**Equivalent Circuit**

*Pin numbers refer to metal can package.

**Typical Application**

*Note: R3 = \( \frac{R1 \times R2}{R1 + R2} \) for minimum temperature drift.

**Typical Performance**

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Regulated Output Voltage</td>
<td>5V</td>
</tr>
<tr>
<td>Line Regulation (( \Delta V_{IN} = 3V ))</td>
<td>0.5mV</td>
</tr>
<tr>
<td>Load Regulation (( \Delta I_L = 50 \text{ mA} ))</td>
<td>1.5mV</td>
</tr>
</tbody>
</table>

*Figure 4. Basic Low Voltage Regulator (\( V_{OUT} = 2 \) to 7 Volts)*
These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Condition</th>
<th>LM723</th>
<th>LM723C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pulse Voltage from ( V^+ ) to ( V^- ) (50 ms)</td>
<td></td>
<td>50V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Continuous Voltage from ( V^+ ) to ( V^- )</td>
<td></td>
<td>40V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input-Output Voltage Differential</td>
<td></td>
<td>40V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Maximum Amplifier Input Voltage (Either Input)</td>
<td></td>
<td>8.5V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Maximum Amplifier Input Voltage (Differential)</td>
<td></td>
<td>5V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current from ( V_Z )</td>
<td></td>
<td>25 mA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Current from ( V_{REF} )</td>
<td></td>
<td>15 mA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Internal Power Dissipation</td>
<td>Metal Can (3)</td>
<td>800 mW</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>CDIP (3)</td>
<td>900 mW</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>PDIP (3)</td>
<td>660 mW</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>LM723</td>
<td>−55°C to +150°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>LM723C</td>
<td>0°C to +70°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>Metal Can</td>
<td>−65°C to +150°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>PDIP</td>
<td>−55°C to +150°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Lead Temperature (Soldering, 4 sec. max.)</td>
<td>Hermetic Package</td>
<td>300°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Plastic Package</td>
<td>260°C</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>ESD Tolerance</td>
<td>1200V</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

(1) “Absolute Maximum Ratings” indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits.

(2) A military RETS specification is available on request. At the time of printing, the LM723 RETS specification complied with the Min and Max limits in this table. The LM723E, H, and J may also be procured as a Standard Military Drawing.

(3) See derating curves for maximum power rating above 25°C.

### ELECTRICAL CHARACTERISTICS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>LM723</th>
<th>LM723C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Line Regulation</td>
<td>( V_{IN} = 12V ) to ( V_{IN} = 15V ) ( -55°C \leq T_A \leq +125°C ) ( 0°C \leq T_A \leq +70°C ) ( V_{IN} = 12V ) to ( V_{IN} = 40V )</td>
<td>0.01</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td>Load Regulation</td>
<td>( I_L = 1 mA ) to ( I_L = 50 mA ) ( -55°C \leq T_A \leq +125°C ) ( 0°C \leq T_A \leq +70°C )</td>
<td>0.03</td>
<td>0.15</td>
<td>0.03</td>
</tr>
<tr>
<td>Ripple Rejection</td>
<td>( f = 50 ) Hz to 10 kHz, ( C_{REF} = 0 ) ( f = 50 ) Hz to 10 kHz, ( C_{REF} = 5 \mu F )</td>
<td>74</td>
<td>86</td>
<td>74</td>
</tr>
</tbody>
</table>

(1) Unless otherwise specified, \( T_A = 25°C \), \( V_{IN} = V^+ = V^- = 12V \), \( V_{OUT} = 5V \), \( I_L = 1 mA \), \( R_{SC} = 0 \), \( C_L = 100 \) pF, \( C_{REF} = 0 \) and divider impedance as seen by error amplifier \( \leq 10 \) kΩ connected as shown in Figure 4. Line and load regulation specifications are given for the condition of constant chip temperature. Temperature drifts must be taken into account separately for high dissipation conditions.

(2) A military RETS specification is available on request. At the time of printing, the LM723 RETS specification complied with the Min and Max limits in this table. The LM723E, H, and J may also be procured as a Standard Military Drawing.

(3) Specified by correlation to other tests.

(4) \( L_1 \) is 40 turns of No. 20 enameled copper wire wound on Ferroxcube P36/22-3B7 pot core or equivalent with 0.009 in. air gap.
### ELECTRICAL CHARACTERISTICS

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Average Temperature Coefficient of Output Voltage (°C)</td>
<td>$-55°C \leq T_A \leq +125°C$</td>
<td>%/°C</td>
</tr>
<tr>
<td></td>
<td>$0°C \leq T_A \leq +70°C$</td>
<td>%/°C</td>
</tr>
<tr>
<td>Short Circuit Current Limit</td>
<td>$R_{SC} = 100Ω$, $V_{OUT} = 0$</td>
<td>mA</td>
</tr>
<tr>
<td>Reference Voltage</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Output Noise Voltage</td>
<td>$BW = 100\ Hz$ to $10\ kHz$, $C_{REF} = 0$</td>
<td>$BW = 100\ Hz$ to $10\ kHz$, $C_{REF} = 5\ \mu F$</td>
</tr>
<tr>
<td>Long Term Stability</td>
<td></td>
<td>%/1000 hrs</td>
</tr>
<tr>
<td>Standby Current Drain</td>
<td>$I_L = 0$, $V_IN = 30V$</td>
<td>mA</td>
</tr>
<tr>
<td>Input Voltage Range</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Output Voltage Range</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Input-Output Voltage Differential</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$\theta_{JA}$</td>
<td>PDIP</td>
<td>105 °C/W</td>
</tr>
<tr>
<td>$\theta_{JA}$</td>
<td>CDIP</td>
<td>150 °C/W</td>
</tr>
<tr>
<td>$\theta_{JA}$</td>
<td>H10C Board Mount in Still Air</td>
<td>165 °C/W</td>
</tr>
<tr>
<td>$\theta_{JA}$</td>
<td>H10C Board Mount in 400 LF/Min Air Flow</td>
<td>66 °C/W</td>
</tr>
<tr>
<td>$\theta_{JC}$</td>
<td></td>
<td>22 °C/W</td>
</tr>
</tbody>
</table>

(5) For metal can applications where $V_Z$ is required, an external 6.2V zener diode should be connected in series with $V_{OUT}$. 
TYPICAL PERFORMANCE CHARACTERISTICS

Load Regulation Characteristics with Current Limiting

![Graph 1](image1.png)

Load & Line Regulation vs Input-Output Voltage Differential

![Graph 2](image2.png)

Current Limiting Characteristics vs Junction Temperature

![Graph 3](image3.png)

Standby Current Drain vs Input Voltage

![Graph 4](image4.png)
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

**Line Transient Response**

- **Vin** = +12V
- **Vout** = +5V
- **IL** = 1 mA
- **TA** = 25°C
- **RSC** = 0

**Output Impedence vs Frequency**

- **Vout** = +5V
- **Vin** = +12V
- **RSC** = 0
- **TA** = 25°C
- **IL** = 50 mA
- **CL** = 1 μF

**Load Transient Response**

- **Vin** = +12V
- **Vout** = +5V
- **IL** = 40 mA
- **TA** = 25°C
- **RSC** = 0

---

Figure 11.

Figure 12.

Figure 13.
MAXIMUM POWER RATINGS

Noise vs Filter Capacitor
(C_{REF} in Circuit of Figure 4)
(Bandwidth 100 Hz to 10 kHz)

Figure 14.

LM723C Power Dissipation vs Ambient Temperature

Figure 15.

LM723C
Power Dissipation vs Ambient Temperature

Figure 16.
Table 1. Resistor Values (kΩ) for Standard Output Voltage

<table>
<thead>
<tr>
<th>Positive Output Voltage</th>
<th>Applicable Figures</th>
<th>Fixed Output ±5%</th>
<th>Output Adjustable ±10% (1)</th>
<th>Negative Output Voltage</th>
<th>Applicable Figures</th>
<th>Fixed Output ±5%</th>
<th>5% Output Adjustable ±10%</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>See (2)</td>
<td>R1</td>
<td>R2</td>
<td>R1</td>
<td>P1</td>
<td>R2</td>
<td>+100</td>
</tr>
<tr>
<td>+3.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>4.12</td>
<td>3.01</td>
<td>1.8</td>
<td>0.5</td>
<td>1.2</td>
<td>3.57</td>
</tr>
<tr>
<td>+3.6</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>3.57</td>
<td>3.65</td>
<td>1.5</td>
<td>0.5</td>
<td>1.5</td>
<td>3.57</td>
</tr>
<tr>
<td>+5.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>2.15</td>
<td>4.99</td>
<td>0.75</td>
<td>0.5</td>
<td>2.2</td>
<td>−6 (3)</td>
</tr>
<tr>
<td>+6.0</td>
<td>Figure 4, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>1.15</td>
<td>6.04</td>
<td>0.5</td>
<td>0.5</td>
<td>2.7</td>
<td>−9</td>
</tr>
<tr>
<td>+9.0</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>1.87</td>
<td>7.15</td>
<td>0.75</td>
<td>1.0</td>
<td>2.7</td>
<td>−12</td>
</tr>
<tr>
<td>+12</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>4.87</td>
<td>7.15</td>
<td>2.0</td>
<td>1.0</td>
<td>3.0</td>
<td>−15</td>
</tr>
<tr>
<td>+15</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>7.87</td>
<td>7.15</td>
<td>3.3</td>
<td>1.0</td>
<td>3.0</td>
<td>−28</td>
</tr>
<tr>
<td>+28</td>
<td>Figure 17, Figure 19, Figure 19, Figure 21, Figure 24, Figure 27 (Figure 19)</td>
<td>21.0</td>
<td>7.15</td>
<td>5.6</td>
<td>1.0</td>
<td>2.0</td>
<td>−45</td>
</tr>
<tr>
<td>+45</td>
<td>Figure 22</td>
<td>3.57</td>
<td>48.7</td>
<td>2.2</td>
<td>10</td>
<td>39</td>
<td>−100</td>
</tr>
<tr>
<td>+75</td>
<td>Figure 22</td>
<td>3.57</td>
<td>78.7</td>
<td>2.2</td>
<td>10</td>
<td>68</td>
<td>−250</td>
</tr>
</tbody>
</table>

(1) Replace R1/R2 in figures with divider shown in Figure 28.
(2) Figures in parentheses may be used if R1/R2 divider is placed on opposite input of error amp.
(3) \( V^+ \) and \( V_{CC} \) must be connected to a +3V or greater supply.

Table 2. Formulae for Intermediate Output Voltages

<table>
<thead>
<tr>
<th>Outputs from +2 to +7 volts (Figure 4 Figure 19 Figure 20 Figure 21 Figure 24 Figure 27)</th>
<th>Outputs from +4 to +250 volts (Figure 22)</th>
<th>Current Limiting</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_{OUT} = \left( V_{REF} \times \frac{R2}{R1+R2} \right) )</td>
<td>( V_{OUT} = \left( \frac{V_{REF}}{2} \times \frac{R2 - R1}{R1} \right) ); ( R3 = R4 )</td>
<td>( I_{LIMIT} = \frac{V_{SENSE}}{R_{SC}} )</td>
</tr>
<tr>
<td>Outputs from +7 to +37 volts (Figure 17 Figure 19 Figure 20 Figure 21 Figure 24 Figure 27)</td>
<td>Outputs from −6 to −250 volts (Figure 18 Figure 23 Figure 25)</td>
<td>Foldback Current Limiting</td>
</tr>
<tr>
<td>( V_{OUT} = \left( V_{REF} \times \frac{R1 + R2}{R2} \right) )</td>
<td>( V_{OUT} = \left( \frac{V_{REF}}{2} \times \frac{R1 + R2}{R1} \right) ); ( R3 = R4 )</td>
<td>( I_{KNEE} = \frac{V_{OUT}}{R_{SC} R4} + \frac{V_{SENSE} (R3 + R4)}{R_{SC} R4} )</td>
</tr>
<tr>
<td></td>
<td></td>
<td>( I_{SHORT , CTX} = \frac{V_{SENSE} (R3 + R4)}{R_{SC} R4} )</td>
</tr>
</tbody>
</table>
TYPICAL APPLICATIONS

Note: R3 = \frac{R1 \cdot R2}{R1 + R2}
for minimum temperature drift.
R3 may be eliminated for minimum component count.

Typical Performance

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Regulated Output Voltage</td>
<td>15V</td>
</tr>
<tr>
<td>Line Regulation ($\Delta V_{IN} = 3V$)</td>
<td>1.5 mV</td>
</tr>
<tr>
<td>Load Regulation ($\Delta I_L = 50 \text{ mA}$)</td>
<td>4.5 mV</td>
</tr>
</tbody>
</table>

**Figure 17. Basic High Voltage Regulator ($V_{OUT} = 7$ to 37 Volts)**

Typical Performance

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Regulated Output Voltage</td>
<td>$-15V$</td>
</tr>
<tr>
<td>Line Regulation ($\Delta V_{IN} = 3V$)</td>
<td>1 mV</td>
</tr>
<tr>
<td>Load Regulation ($\Delta I_L = 100 \text{ mA}$)</td>
<td>2 mV</td>
</tr>
</tbody>
</table>

**Figure 18. Negative Voltage Regulator**
Typical Performance

Regulated Output Voltage +15V
Line Regulation ($\Delta V_{IN} = 3V$) 1.5 mV
Load Regulation ($\Delta I_L = 1A$) 15 mV

Figure 19. Positive Voltage Regulator (External NPN Pass Transistor)

Typical Performance

Regulated Output Voltage +5V
Line Regulation ($\Delta V_{IN} = 3V$) 0.5 mV
Load Regulation ($\Delta I_L = 1A$) 5 mV

Figure 20. Positive Voltage Regulator (External PNP Pass Transistor)
Typical Performance

Regulated Output Voltage

Line Regulation ($\Delta V_{IN} = 3V$)  
0.5 mV

Load Regulation ($\Delta I_L = 10 mA$)  
1 mV

Short Circuit Current  
20 mA

Figure 21. Foldback Current Limiting

Typical Performance

Regulated Output Voltage  
+5V

Line Regulation ($\Delta V_{IN} = 20V$)  
15 mV

Load Regulation ($\Delta I_L = 50 mA$)  
20 mV

Figure 22. Positive Floating Regulator
Regulated Output Voltage
Line Regulation ($\Delta V_{IN} = 20V$)
Load Regulation ($\Delta I_L = 100 mA$)

Figure 23. Negative Floating Regulator

Regulated Output Voltage
Line Regulation ($\Delta V_{IN} = 30V$)
Load Regulation ($\Delta I_L = 2A$)

Figure 24. Positive Switching Regulator
Typical Performance

Regulated Output Voltage

−15V

Line Regulation ($\Delta V_{IN} = 20V$)

8 mV

Load Regulation ($\Delta I_L = 2A$)

6 mV

Figure 25. Negative Switching Regulator

Note: Current limit transistor may be used for shutdown if current limiting is not required.

Regulated Output Voltage

+5V

Line Regulation ($\Delta V_{IN} = 3V$)

0.5 mV

Load Regulation ($\Delta I_L = 50 mA$)

1.5 mV

Figure 26. Remote Shutdown Regulator with Current Limiting
Regulated Output Voltage

Line Regulation ($\Delta V_{\text{IN}} = 10\text{V}$) $0.5\text{ mV}$

Load Regulation ($\Delta I_{\text{L}} = 100\text{ mA}$) $1.5\text{ mV}$

**Figure 27. Shunt Regulator**

**Figure 28. Output Voltage Adjust**

(1) Replace R1/R2 in figures with divider shown in Figure 28.
Schematic Diagram

[Diagram of the LM723 and LM723C circuit diagram]
## REVISION HISTORY

Changes from Revision B (April 2013) to Revision C

<table>
<thead>
<tr>
<th></th>
<th>Page</th>
</tr>
</thead>
<tbody>
<tr>
<td>Changed layout of National Data Sheet to TI format</td>
<td>15</td>
</tr>
</tbody>
</table>
### PACKAGING INFORMATION

<table>
<thead>
<tr>
<th>Orderable Device</th>
<th>Status (1)</th>
<th>Package Type</th>
<th>Package Drawing</th>
<th>Pins</th>
<th>Package Qty</th>
<th>Eco Plan (2)</th>
<th>Lead/Ball Finish</th>
<th>MSL Peak Temp (3)</th>
<th>Op Temp (°C)</th>
<th>Top-Side Markings (4)</th>
<th>Samples</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM723CH</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM723CH</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CH/NOPB</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>POST-PLATE</td>
<td>Level-1-NA-UNLIM</td>
<td>0 to 70</td>
<td>LM723CH</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CN</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>NFF</td>
<td>14</td>
<td>25</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>0 to 70</td>
<td>LM723CN</td>
<td>Samples</td>
</tr>
<tr>
<td>LM723CN/NOPB</td>
<td>ACTIVE</td>
<td>PDIP</td>
<td>NFF</td>
<td>14</td>
<td>25</td>
<td>Green (RoHS &amp; no Sb/Br)</td>
<td>CU SN</td>
<td>Level-1-NA-UNLIM</td>
<td>0 to 70</td>
<td>LM723CN</td>
<td>Samples</td>
</tr>
<tr>
<td>USR7723312</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>-55 to 150</td>
<td>LM723H</td>
<td>Samples</td>
</tr>
<tr>
<td>USR7723393</td>
<td>ACTIVE</td>
<td>TO-100</td>
<td>LME</td>
<td>10</td>
<td>500</td>
<td>TBD</td>
<td>Call TI</td>
<td>Call TI</td>
<td>-55 to 150</td>
<td>LM723H</td>
<td>Samples</td>
</tr>
</tbody>
</table>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.
**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.
**OBSOLETE:** TI has discontinued the production of the device.

(2) **Eco Plan** - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check [http://www.ti.com/productcontent](http://www.ti.com/productcontent) for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material).

(3) **MSL, Peak Temp.** – The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) **Top-Side Markings**: Multiple Top-Side Markings will be inside parentheses. Only one Top-Side Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Top-Side Marking for that device.

Addendum-Page 1
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NOTES:  

A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. Leads in true position within 0.010 (0,25) R @ MMC at seating plane.
D. Pin numbers shown for reference only. Numbers may not be marked on package.
E. Falls within JEDEC MO–006/TO-100.
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